**Industrial Electronics Course Code EE-301**

**Department of Electronics Engineering**

**University of Engineering & Technology**

**Taxila**

**ASSIGNMENT 02**

**Due Date: 8th April, 2015**

**SEMESTER: SPRING 2015**

**Session: 2K13**

**Total Marks: 30**

**Question 1:**

Design Basic and Universal gates via Relay. Also write there truth tables. [10 Marks]

**Question 2:**

Design following circuits.

1. 2x4 Decoder [2.5 Marks]
2. 3x8 Decoder [2.5 Marks]
3. Multiplexer (4 \_1) [2.5 Marks]
4. De-multiplexer (1\_4) [2.5 Marks]
5. Also write Ladder Logic of (i), (ii), (iii) & (iv). [10 Marks]

Good luck